

Implementation of hardware time stamps for use in WIFI time synchronization with the Analog Devices FMCOMMS4/5 RF transceiver evaluations Board and the Xilinx ZedBoard Zynq-7000

- Language: German or English
- Prerequisites:
 - Basics of digital systems
 - Basics of network technology
 - VHDL/Verilog programming
 - Beneficial: Experience with embedded devices (e.g. Raspberry PI, Arduino etc.), linux basics and Xilinx ISE/Vivado IDE
- Tasks:
 - Make familiar with the AD FMCOMMS4/5 evaluations board and the ZedBoard
 - Selection of a suitable SDR project (e.g. gnuradio/open-sdr)
 - Test setup with all necessary components
 - Implementation and evaluation of time stamp functionality on FPGA
- Literature:
 - <http://zedboard.org/sites/default/files/documentations/GS-AES-Z7EV-7Z020-G-V7-1.pdf>
 - <https://www.analog.com/media/en/technical-documentation/data-sheets/AD9364.pdf>
 - <https://wiki.gnuradio.org/index.php/Hardware>
 - <https://github.com/open-sdr>
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